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1. [Distributed Computing with COM+ \(pt 1\)](#)

... Client machines may have limited access to server pool (eg, **internet/web**).

... Command Line to **Launch** Debugger/Process. ... High-**watermark** throttles number of clients: ...

www.aspxnet.de/COM_RCD/Powerpoints/Distributed%20Computing%20with%20COM+%20 - 0

2. [untitled](#)

... There is no way to tell which **watermark** was the ... want to protect the images we publish o ... at securely delivering high quality audio over the **Internet**. ...

www.cis.upenn.edu/~sahuguet/PLAN/piracy.ps.gz - 0 B

3. [Augmented Reality, What for?](#)

... the Info- sphere comprised of the **Internet**, Intranets, Extranets ... profound as the World W However, a ... the appropriate label and **watermark** detection tools ...

www.inigraphics.net/publications/topics/1998/issue1/1_98Artikel.pdf - 0 B

4. [Transatlantic Development Environment \(Trade\)](#)

... Inte- grated services in the **Internet** architectu- re: an ... The **Web**-read command that was earlier may be ... could be per- formed by the **machine** operator rather ...

www.inigraphics.net/publications/topics/1998/issue3/3_98Artikel.pdf - 0 B

5. [W + Structure = Knowledge](#)

... the skills of their developers to **launch** a powerful ... under de- velopment by the **Internet E** Task ... In publications by the World Wide **Web** Consortium (W 3 C ...

www.hpl.hp.com/techreports/96/HPL-96-99.pdf - 0 B

6. [untitled](#)

... The same **watermark** facility can also be used to make users aware of. ... **web** photograph. finishing services are of-fered over the **Internet** using a. W. 3. ...

www.hpl.hp.com/techreports/96/HPL-96-99.ps - 0 B

7. [IEEE Sample Articles](#)

... and concealed within the digital object using a **watermark**. ... of a resource on the World Wi compact ... for a resource available on the **Internet**, now defined ...

www.spectrum.ieee.org/pubs/mags/9908/87proc07-paskin.html - 101 KB

8. [2321 MacAdvantage FINAL](#)

... the Mac OS will find and **launch** Excel ... PageMill is a full WYSIWYG **Web** page author- ing to SiteMill automatically updates documents as **Internet** links change ...

www.abc.it/Apple/Product_Info/MacAdvantage/50MacAdvantages.pdf - 0 B

9. Swiss Priority Programme Project Descriptions Approved Projects ...

... 37 5003-051321 Pun: AB-**Web**: Active audio **internet** ... has created various project groups the Global ... an electronic platform accessible by **Internet** protocol ...
spp-ics.snf.ch/spp_ics/SPPICS-Feb-2000-projectdesc.pdf - 0 B

10. The state of the art in library access

... to full text local or remote documents from **Web** opacs, with the facility to **launch** a reader .. incorporate records to evanescent **Internet** resources into a ...
severn.dmu.ac.uk/elise/el2_dels/s_candle.doc - 0 B

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1 VARIATIONS: a digital music library system at Indiana University

Jon W. Dunn; Constance A. Mayer

August 1999 Proceedings of the fourth ACM conference on Digital libraries

Full text available: pdf(122.41 KB) Additional Information: full citation, references, citings, index terms

Keywords: digital audio, digital libraries, music libraries

2 On linear potential functions for approximating Bayesian computations

Eugene Santos

May 1996

Journal of the ACM (JACM), Volume 43 Issue 3

Full text available: pdf(1.95 MB)

Additional Information: full citation, abstract, references, i

Probabilistic reasoning suffers from NP-hard implementations. In particular, the a to the computations is often overwhelming. For example, the size of conditional p long been a limiting factor in the general use of these networks. We present a new information given. This approach avoids being overwhelmed by essentially compr

Keywords: artificial intelligence, data compaction and compression, integer progr recognition, probabilistic reasoning, uncertainty

3 Tamper-resistant whole program partitioning

Tao Zhang, Santosh Pande, Antonio Valverde

June 2003 ACM SIGPLAN Notices , Proceedings of the 2003 ACM SIGPLAN conference
embedded systems, Volume 38 Issue 7

Full text available:  pdf(444.16 KB)

Additional Information: full citation, abstract, refere


Due to limited available memory (of the order of Kilobytes) on embedded devices approach of partitioning the whole program when it does not fit in the memory. The server on demand into the embedded device just before execution. We devise of the program such that no information regarding the control flow behavior of the called tamper ...

Keywords: mobile code, program partitioning, ramper resistance, smart card

4 A personal view of the personal work station: some firsts in the Fifties

Douglas Ross

January 1986 Proceedings of the ACM Conference on The history of personal workstat

Full text available:  pdf(4.26 MB)

Additional Information: full citation, references, index terms

5 A hardware/software co-design flow and IP library based on simulink

L. M. Reyneri, F. Cucinotta, A. Serra, L. Lavagno

June 2001 Proceedings of the 38th conference on Design automation

Full text available:  pdf(119.94 KB)

Additional Information: full citation, abstract, refere

This paper describes a design flow for data-dominated embedded systems. We use an environment for functional specification and algorithmic analysis. We developed a parameterized by design choices such as implementation (software, analog or digital), resolution, S/N ratio). Each block is equipped with empirical models for cost (cost, energy), based ...

6 Self-test methodology for at-speed test of crosstalk in chip interconnects

Xiaoliang Bai, Sujit Dey, Janusz Rajski

June 2000 Proceedings of the 37th conference on Design automation

Full text available:  pdf(113.37 KB)

Additional Information: full citation, abstract, references

The effect of crosstalk errors is most significant in high-performance circuits, mainly in System-on-Chip interconnects. This paper describes a self-test methodology that we have developed to enable on-chip testing of the interconnect with a linear number of test patterns. To enable self-t

7 Prototyping, verification, and test: Reducing pin and area overhead in fault-t

Fernanda Lima, Luigi Carro, Ricardo Reis

February 2003 Proceedings of the 2003 ACM/SIGDA eleventh international symposiu

Full text available:  pdf(328.13 KB)

Additional Information: full citation, abstract, referen




This paper proposes a new high-level technique for designing fault tolerant syste
modifications in the FPGA architecture. Traditionally, TMR has been successfully a
which are likely to occur in space applications. However, TMR comes with high ar
proposed technique was specifically developed for FPGAs to cope with transient fi
logic, while ...

Keywords: FPGA, fault-tolerance

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1 Scrambling technique for video object watermarking resisting to MPEG
Vassaux, B.; Nguyen, P.; Baudry, S.; Bas, P.; Chassery, J.-M.;
 Video/Image Processing and Multimedia Communications 4th EURASIP-IEEE Regi
 International Symposium on VIPromCom , 16-19 June 2002
 Page(s): 239 -244

[\[Abstract\]](#) [\[PDF Full-Text \(546 KB\)\]](#) **IEEE CNF**

2 A practical method for watermarking Java programs
Monden, A.; Iida, H.; Matsumoto, K.; Inoue, K.; Torii, K.;
 Computer Software and Applications Conference, 2000. COMPSAC 2000. The 24th
 Annual International , 25-27 Oct. 2000
 Page(s): 191 -197

[\[Abstract\]](#) [\[PDF Full-Text \(644 KB\)\]](#) **IEEE CNF**

3 A DCT domain visible watermarking technique for images
Mohanty, S.P.; Ramakrishnan, K.R.; Kankanhalli, M.S.;
 Multimedia and Expo, 2000. ICME 2000. 2000 IEEE International Conference on ,
 Volume: 2 , 30 July-2 Aug. 2000
 Page(s): 1029 -1032 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(312 KB\)\]](#) **IEEE CNF**

4 Protecting ownership rights through digital watermarking
Berghel, H.; O'Gorman, L.;
 Computer , Volume: 29 Issue: 7 , July 1996
 Page(s): 101 -103

[\[Abstract\]](#) [\[PDF Full-Text \(676 KB\)\]](#) **IEEE JNL**

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